

Mr. Ravishankar Holla

Assistant Professor and Co-coordinator for Advanced Diploma in Automotive Mechatronics

Educational Qualification	B. E. (Telecommunication Engineering), M.Tech. (Computer Network Engineering). (Ph.D.- Machine Learning / Signal Processing for Sensors)
Experience	Teaching: 16 Years. Industry: 3 Years
Area of Interest	Machine learning / Signal Processing based Embedded system, Automotive Embedded Systems
Email ID	ravishankarholla@rvce.edu.in

Date of Joining at RVCE: 24th January 2013

Subjects Handled: Embedded systems, Data Science, Machine Learning

No. of Projects guided to UG Students: 10

No. of Projects guided to PG Students: 6

Research Projects:

1. Collaboration with NcFlexe, IIT Kanpur for development of signal processing algorithm for improving selectivity of chemical sensors.
2. Contributor to SPIRE lab project on Speech and Machine Learning in collaboration with IISc, Bangalore. Project funded by Bill-Melinda Gates foundation.
[https://respin.iisc.ac.in/contributors/academics/r.v.%20college%20of%20engineering%20\(rvce\)](https://respin.iisc.ac.in/contributors/academics/r.v.%20college%20of%20engineering%20(rvce))

Consultancy Projects:

1. Machine Learning Based projects (5 projects) for SAMSUNG Research India Pvt Ltd.
2. Training & Consultancy on Sensors, actuators and Systems with M/s MERITOR COMMINS
3. Training & Consultancy on Embedded systems and circuits for EV with M/s E-drive Technologies
4. Training & Consultancy on Embedded systems for BHARATH ELECTRONICS LTD (BEL)
5. Machine Learning and Mobile based work with 3M technologies.
6. Embedded system with M/s SWAN SORTERS
7. Embedded system with M/s TELLER TECHNOLOGIES PVT.LTD
8. Machine Learning and Embedded System with M/s EAPL
9. Working with GSI pool Finishes – Pennsylvania, USA for Machine Learning based pool water quality management

Publication Details:

1. <https://scholar.google.com/citations?user=xEILxCUAAAAJ&hl=en>
2. <https://www.scopus.com/authid/detail.uri?authorId=57203958358>

Major Publications:

1. V Anjaneyappa;Varuna M;Archana M R;Sunil S;Ravishankar Holla;Shrish Shrinath Vaidya;Mahendra B M, "Smart Phone for Measurement of Reflectivity of Road Pavement Markings", *2024 8th International Conference on Computational System and Information Technology for Sustainable Solutions (CSITSS)*. IEEE, 2024.
2. Rajani Katiyar;Prasenjit Chakraborty;Ravi Surana;Ravishankar Holla;Sanka Sanjana;Sathvik Acharya;Sonia Singh B;Yash Agrawal, "Real-Time Video PLC Using In-Painting", *16th International Conference on COMMunication Systems & NETWORKS (COMSNETS)*, IEEE, 2024
3. A. Ragi, Nikhil M., Ravishankar Holla, and G. Manju. "Predicting water quality parameters using machine learning." *2019 4th International Conference on Recent Trends on Electronics, Information, Communication & Technology (RTEICT)*. IEEE, 2019.
4. Holla, A. Ravishankar, Ananth Noorithaya, and M. Uttarakumari. "Characterisation of TFT Sensors for Chemical Sensing Applications." *Proceedings of the International Conference on Nano-electronics, Circuits & Communication Systems*. Springer Singapore, 2017
5. Omprakash, S. S., SK Naveen Kumar, and Ravishankar Holla. "Titanium dioxide and Zinc oxide as a dielectric material for application in TFT's." *Materials Today: Proceedings* 5.4 (2018): 10833-10838.
6. Holla, Ravishankar, Akash Shanbhag, and Karmugilan Murugu. "In-Vehicle Communication and Data acquisition system." *Authorea Preprints* (2023).
7. Sreevidya, S., Ravishankar Holla, and Roopaka Raghu. "Low power physical design and verification in 16nm finfet technology." *2019 3rd International conference on Electronics, Communication and Aerospace Technology (ICECA)*. IEEE, 2019.
8. Anjaneyappa, V., Varuna, M., Archana, M. R., Sunil, S., Holla, R., Vaidya, S. S., & Mahendra, B. M. (2024, November). **Smart Phone for Measurement of Reflectivity of Road Pavement Markings**. In *2024 8th International Conference on Computational System and Information Technology for Sustainable Solutions (CSITSS)* (pp. 1-5). IEEE.
9. Madhura, G., and Ravishankar Holla. "UVM Based Design Verification of Interconnect Block." *Proceeding of Fifth International Conference on Microelectronics, Computing and Communication Systems: MCCS 2020*. Springer Singapore, 2021.
10. Holla, Ravishankar, and G. Nithya. "Design and Power Optimization of a 64-Bit SIMD Processor with Efficient Carry-Bit Handling." .
11. Ram Prasad, J. M., B. S. Kariyappa, and Ravishankar Holla. "Design and Implementation of Flash ADC for Low Power Applications." *IOSR Journal of VLSI and Signal Processing (IOSR-JVSP) Volume 4* (2014): 41-46.
12. Pushparaj, Vinay, and Ravishankar Holla. "Architectural Design of On-Rotary CAN BUS Debugging Tool for CT Machine."