

Mr. S. Praveen

Assistant Professor.

Educational Qualification	B. E. (Electronics & Communication Engineering), M.Tech. (VLSI Design & Embedded Systems). (Ph.D.- Overcoming Power quality issues in Voltage Regulators/ Biomedical Applications)
Experience	Teaching: 10 Years. Industry: 5 Years
Area of Interest	VLSI Design, Verification and Embedded Systems
Email ID	praveens@rvce.edu.in

Date of Joining at RVCE: 27.07.2015

Subjects Handled: Embedded systems, Multicore Processing, High Performance Computing, System Verilog, Digital System Design using Verilog HDL, Basic Electronics, Elements of Electronics Engineering.

No. of Projects guided to UG Students: 10

No. of Projects guided to PG Students: 6

Research Projects:

1. Collaboration with NcFlexe, IIT Kanpur for development of signal processing algorithm for improving selectivity of chemical sensors.
2. Contributor to SPIRE lab project on Speech and Machine Learning in collaboration with IISc, Bangalore. Project funded by Bill-Melinda Gates foundation.
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Consultancy Projects:

1. Training the data sets(Annotation) using Machine Learning for automated vehicle (2 projects) for SAMSUNG Research India Pvt Ltd.
2. Training & Consultancy on Embedded systems for BHARATH ELECTRONICS LTD (BEL)
3. Training & Consultancy on Embedded systems and circuits for EV with M/s E-drive Technologies
4. Embedded system with M/s SWAN SORTERS

Publication Details:

1. <https://scholar.google.com/citations?user=IGPSNR0AAAAJ&hl=en>
2. <https://www.scopus.com/authid/detail.uri?authorId=57203958358>