

P Narashimaraja

Assistant Professor

Educational Qualification	B.E (ECE), M.Tech.(VLSI Design)
Experience	Teaching: 18 Years Industry : Nil
Area of Interest	Analog IC Design, RF IC Design
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Date of Joining at RVCE : 22nd June 2007

Subjects Handled : Analog IC Design, Digital IC Design, Digital VLSI Design, Analog Micro-Electronics, Analog Mixed mode VLSI.

Publication Details

International Journals:

1. Arunkumar. P. Chavan, Rekha. G, P. Narashimaraja , “Design of a 1.5-V, 4-bit Flash ADC using 90nm Technology ”, *International Journal of Engineering and Advanced Technology (IJEAT)* , ISSN: 2249 – 8958, Volume-2, Issue-2, December 2012 .
2. Arunkumar. P. Chavan, Rekha. G, P. Narashimaraja , “An Efficient Design of 3bit and 4bit Flash ADC”. *International Journal of Computer Applications* 61(11):32-37, January 2013. Published by Foundation of Computer Science, New York, USA.
3. Mangesh B Kondalkar, Arunkumar P Chavan, P Narashimaraja “Improved Fault Tolerant Sparse KOGGE Stone ADDER”. *International Journal of Computer Applications (IJCA) Journal*, Volume 75 - Number 10, ISBN : 973-93-80877-10-8, August 23, 2013.
4. Suraj, P Narashimaraja. “Design of an Efficient Arithmetic Logic Unit using Quantum Dot Cellular Automata”, *International Journal of Research*, DOI: 16.10089/IJR, ISSN: 2236-6124, June 2020.
5. Ravikiran S E , P. Narashimaraja, “DFT Flow for Automotive SOC”, *International Journal of Advanced Science and Technology*, ISSN: 2005-4238 IJAST, June 2020.
6. A V Skandashree, P Narashimaraja, Ankush Raina, “Design and Development of Battery Management System for Fuel Cells”, *Journal of Information and Computational Science*, ISSN: 1548-7741, June 2020.
7. Kiran S, P Narashimaraja, Ankush Raina, “A Comparision of IMC and PID controller using Matlab/Simulink program”, *INTERNATIONAL RESEARCH JOURNAL OF ENGINEERING AND TECHNOLOGY (IRJET)*, Pg. 1990-1993, Volume: 07 Issue: 06, ISSN: 2395-0056, June 2020.

8. *Sumanth Sajjanar, Suraj Ullur, Mr. P Narashimaraja, ""*, INTERNATIONAL RESEARCH JOURNAL OF ENGINEERING AND TECHNOLOGY (IRJET), Pg. 7066-7070, Volume: 07 Issue: 05, ISSN: 2395-0056, May 2020.

International Conferences:

1. Arunkumar. P. Chavan, Rekha. G, P. Narashimaraja , “An Efficient 90nm CMOS Flash ADC with Hybrid Architecture”, International Conference on Information & Communication Engineering (ICICE-2013), ISBN:978-81-31703-83-2, 28th June 2013.
- 2.

National conferences:

1. Panpalia Jagdish G, P Narashimaraja, “Design and Performance Analysis of Two-Stage Operational Transconductance Amplifier in 180nm CMOS Technology”, National Conference on “Research Challenges in Power, Control, Communication and Instrumentation leading to Sustainable Technologies”, [NCPCCI-15]