

# Faculty Profile

## Brief Profile

I am Smriti Srivastava, an Associate Professor in the Department of Computer Science and Engineering at RV College of Engineering (RVCE), Bengaluru, India. I have been a part of RVCE since 2014. Prior to joining RVCE, I worked as a lecturer, assistant professor in various engineering colleges.



## Personal Information

**Name:** Dr. Smriti Srivastava

**Designation:** Associate Professor

**Department:** Department of Computer Science and Engineering

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**Phone:** 9632751133

**Google Scholar :** <https://scholar.google.co.in/citations?user=pp9QEzgAAAAJ&hl=en>

**Scopus:** <http://www.scopus.com/authid/detail.url?authorId=57842639400>

**ORCID:** <https://orcid.org/0000-0003-3338-8374>

## Domain of Expertise

Network on chip, Multi-core Architecture.

## Research Focus

**Primary Area:** Networks, Artificial Intelligence (AI), Machine Learning (ML)

**Allied Areas:** Network on chip.

## Academic Qualifications

**Ph.D. :** CSE, VTU, Belagavi, 2024. **Thesis Title:** Intelligent Hybrid NoC for Many Core Processor Architecture

**M.Sc./M.Tech:** Computer Science and Engineering, Amity University, 2013

**B.E./B.Tech:** Computer Science and Engineering, U P Technical University, U.P. 2009.

## Professional Experience

Experience

S.No	Institute/College/Industry	Job Title	Duration
1.	Fort Institute of Technology, Meerut, U.P.	Lecturer	Oct 2009- Jan 2011
2.	IIMT Engineering College, Meerut, U.P.	Assistant Professor	Jan 2011- April 2013
3	R.K Institute of Management and Computer Science, Bangalore	Assistant Professor	July 2013-April 2014
4	RV College of Engineering Bangalore	Assistant Professor, Associate Professor	2014-till date

## Publications & Patents

### Journal Publications

Paramjeet Rawat, Jyoti Gupta, Smriti Srivastava, “Survey of Fault Tolerance in Wireless Mesh Network”, International Journal of Science and Advanced Technology (ISSN 2221-8386) Volume 1 Issue 9 Nov 2011 Pg 272-275, **Dhaka**.

Smriti Srivastava, Anant Kumar Jaiswal, Paramjeet Rawat, “Gateway Placement Approach: A Survey”, International Journal of Engineering and Innovative Technology (ISSN 2277-3754), Vol 1, Issue 4, April 2012, **Florida, US**.

Smriti Srivastava, Anant Kumar Jaiswal, “Clustering based Load Balanced Gateway Placement Approach”, International Journal of computer application (ISSN 973-93-80872-85-9), Volume 6, Number 5, Feb 2013, **US**.

Mamatha T, Smriti Srivastava, Dr. Aishwarya, “Survey on Different Energy Efficient Routing Protocols for Wireless Sensor Network”, IARJSET (ISSN 2394-1588), Vol 2, issue 5, May 2015.

Mamatha T, Smriti Srivastava, Dr. Aishwarya, “Issues and challenges in the Integration of Wireless sensor Network and IOT”, IJCA Volume-146 No.7, July 2016.

Ganeshprasad Biradar, Aditya Giridharan, Chetan K P, Smriti Srivastava, “Review on recent Trends in virtualization and its Impact”, JETIR, Volume 6, Issue 4, April 2019. (ISSN-2349-5162), Page No-36-40.

Shubham Milind Phall, Smriti Srivastava, “An Analysis of Machine Learning Methods for Ranking in Recommendation Systems”, International Research Journal of Engineering and Technology (IRJET), Volume: 07 Issue: 05, May 2020, e-ISSN: 2395-0056, p-ISSN: 2395-0072. Page No-1-5.

Sameeksha Shetty, Smriti Srivastava, “Security Tickets Monitoring System”, International Research Journal of Engineering and Technology (IRJET), Volume: 07 Issue: 06, June 2020, e-ISSN: 2395-0056, p-ISSN: 2395-0072, Page No-1498-1501.

Smriti Srivastava, Dr. Minal Moharir, “An Overview of Communication Methods on WiNoC”, International Journal of Advanced Science and Technology Vol. 29, No. 03, (2020), pp. 12876 – 12883.

Srivastava, S., Moharir, M., & Gunisetty, S. (2024). Performance analysis of congestion-aware

Q-routing algorithm for network on chip. IAES International Journal of Artificial Intelligence

(IJ-AI), 13(1), 798. <https://doi.org/10.11591/ijai.v13.i1.pp798-806>. **(Q2 Paper)**

### Conference Papers

1. Smriti Srivastava, Adithi Viswanath, Krithika Venkatesh, and Dr. Minal Moharir, “TDMA Based Adaptive Multicasting in Wireless NoC”, Springer Proceedings, 6th International Conference on Inventive Systems and

Control ICISC,6-7 January 2022 organised by JCT College of Engineering,Coimbatore.

2. Srivastava, S., Shaikh, M. A., G, S., & Moharir, M. (2022). Intelligent congestion control for NoC architecture in Gem5 simulator. 15th IEEE 15th Int. Sym. on Embed. MCSoc. <https://doi.org/10.1109/mcsoc57363.2022.00062>

## Patents :

**Topic:** INNOVATIVE VISUALIZATION TOOL SHAPE FOR COMPLEX DATA ANALYSIS

Application No- 450676-001

Date of filing- 07/03/2025

## R & Grants & Consultancy Projects

## Ongoing & Completed Research Projects

## Ongoing & Completed Consultancy Projects

## Professional Memberships

## Awards & Recognitions

Successfully completed an AICTE approved Four Week FDP conducted by IIT Bombay on – “Use of ICT in Education for Online and Blended Learning” and received “**Award of Excellence (Cash prize of 5000) by SAP India Pvt. Ltd.**”

## Student Supervision

Ph.D. Candidates: NA

M.Tech/M.Sc. Students: 01

Undergraduate Research Mentees: 30+

## Professional Roles

## Teaching

**Core Courses:** [Current semester]

Compiler Design (CS363AI)

Principal of Programming Using C(CS222AI)

## Advanced/Lab Courses:s

Compiler Design Lab

## Professional Roles

## Responsibilities

**Academic:**

**Administrative:** Department Invited Talk/Workshop/Seminar/FDP conducted File.

## External Connect